/6	OFE UCITE	
	OCI 5 1 5005	را د د ی د
	PATENT & TP	7.
	10	

93 Co

25								
STATUS REQUEST						Docket Number		
							CM03422J	
In re Applica	ation of	STENGEL, F	ROBERT E.		<del></del> :			
Application	Number	10/036,550	2.44	File Date	12/21/01			
Title	ţ.	A METHOD AND STRUCTURE FOR INTEGRATED CIRCUIT INTERFERENCE ISOLATION ENHANCEMENT						
Group Art U	nit	NOT ASSIGN	VED	Examiner	NOT ASSI	GNED		
Status of the above-identified application is requested.  The last paper(s) received by Applicant(s) was:  Official Filing Receipt  Other - Stamped Post card and Notice of Recordation of Assignment  The last paper(s) submitted by Applicant(s) was:								
The last paper	(S) SUDII		ant(s) was.					
Since the trans States Patent a	mittal/reand Trac	ceipt of said pademark Office.	aper(s), no oth	er communi	cations have	been receive	d from the United	
Attorney or Ago	ent	Barbara R. Do	outre					
Printed Name Customer Num	nher:	Attorney 24273			Registratio	n Number	39,505	
Signature Date		10/15/02	lun K	P. 1	Jai			
I hereby certify deposited with mail in an enve	the Unite	ed States Post	with any paper al Service on the	he date sho	as being atta wn below wit	h sufficient po	stage as first-class	
Name	Maria	E. Rodriguez						
Signature		Main &			Date		10/15/02	